



EV372468649

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/615,287
Filing Date July 7, 2003
Inventor Kunal R. Parekh
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner H. Jey Tsai
Attorney's Docket No. MI22-2260
Title: Methods of Forming Memory Circuitry

Do not
rebut
RST
w/ptz/ep

RESPONSE TO SEPTEMBER 27, 2004 FINAL OFFICE ACTION
PRELIMINARY AMENDMENT TO ACCOMPANY RCE FILING

To: Mail Stop RCE
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

VIA EXPRESS MAIL

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Responsive to the Final Office Action dated September 27, 2004, Applicant
amends and remarks as follows:

AMENDMENTS

EV372468167